UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.　　　　: 6,901,159 B2　　　　　　　　　　　　　　Page 1 of 1
APPLICATION NO. : 09/774552
DATED　　　　　　 : May 31, 2005
INVENTOR(S)　　　 : Baertsch et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 53, Line 43, delete "FUiLL" and insert -- FULL --, therefor.

In Column 53, Line 51, delete "FLL)" and insert -- FULL) --, therefor.

In Column 62, Line 19, delete "Flfag" and insert -- Flag --, therefor.

In Column 70, Line 4, delete "Modem" and insert -- Modern --, therefor.

In Column 78, Line 31, delete "DIL" and insert -- DLL --, therefor.

In Column 86, Line 29, in Claim 9, delete "nods" and insert -- node --, therefor.

In Column 88, Line 5, in Claim 33, delete "(318)" and insert -- (316) --, therefor.
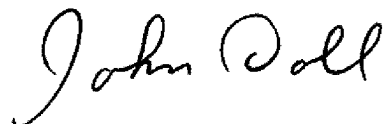
In Column 88, Line 8, in Claim 34, after "host processor" delete "to".

In Column 88, Line 15, in Claim 34, delete "from an image detection bus" before "at a first".

In Column 88, Line 52, in Claim 39, delete "butter" and insert -- buffer --, therefor.

Signed and Sealed this

Twenty-eighth Day of July, 2009

JOHN DOLL
Acting Director of the United States Patent and Trademark Office